PATENT ABSTRACTS OF JAPAN

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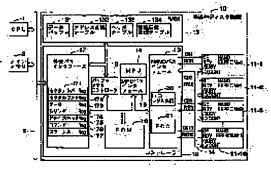
(54) SEMICONDUCTOR DISK DEVICE

(57)Abstract:
PURPOSE: To improve the reliability of storage data in a
flush EEPROM.
CONSTITUTION: An EEC arithmetic circuit 21
calculates data strings transferred from a data buffer
131 in the unit of 256 bytes from a processor 14 to
generate an ECC corresponding to the data string. The
generated ECC is added to the data string in 256 bytes
and transferred to a data register of flush EEPROMs 11-
1-11-16. Thus, even when a defective cell of the same
bit location of plural pages of the flush EEPROMs takes
place, the defective cell including the data string being
an object of ECC operation is only one. Thus, error
check and correction are implemented by the usual
simple ECC arithmetic operation without using a
complicated ECC arithmetic equation having a high data
recovery capability and then the semiconductor disk
device 10 with high storage reliability and capable of high
speed access is realized.

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